



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of

SHIMAMOTO *et al.*

Application Number: 10/020,314

Filed: December 18, 2001

For: FABRICATING METHOD OF
SEMICONDUCTOR INTEGRATED CIRCUITS

Attorney Docket No. HITA.0143

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

)
)
) Art Unit 2823
) Examiner
) Nguyen, Khiem D.
)
)
)

PETITION FOR EXTENSION OF TIME

Sir:

Applicants request that the time for taking action in this case be extended pursuant to 37 C.F.R. § 1.136(a) for **three (3) months**. The fee set in 37 C.F.R. § 1.17 for the extension of time is **\$1,020.00**.

Please charge any additional fee required for this extension of time to Deposit Account 08-1480. A duplicate copy of this paper is enclosed.

Also filed is a *Request for Continued Examination and a Preliminary Amendment*.

Respectfully submitted,

Stanley P. Fisher
Registration Number 24,344

Juan Carlos A. Marquez
Registration Number 34,072

REED SMITH LLP
3110 Fairview Park Drive, Suite 1400
Falls Church, Virginia 22042
(703) 641-4200
August 9, 2006

08/11/2006 SDEHQB01 00000005 10020314
02 FC:1253

1020.00 OP

Adjustment date: 08/11/2006 SDEHQB01
08/10/2006 J14001 00000004 10020314
02 FC:1253
02 FC:1253 -1020.00 OP
1020.00 OP